

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2291	(438/128).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 20:34
L2	229	(455/252.1).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 20:34
L3	1240	(455/333).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 20:34
L4	0	(455/550).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 20:34
L5	1519	(455/558).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 20:34
L6	2	trainor-alan.in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 20:44
S1	2	((("5258325") or ("5731945"))).PN.	USPAT	OR	OFF	2010/04/08 18:12
S2	4247015	(integrat\$3 adj circuit\$1) or die\$1 or chip\$1 or semiconductor\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12

S3	1461082	{signal with condition\$3} or {amplif\$4 adj circuit\$1} or {amplifier\$1 or (power adj amplifier\$1) or (low adj noise adj amplifier\$1)}	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S4	1111154	"PA" or "LNA"	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 18:12
S5	2465807	S3 or S4	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S6	3420	{andillary with circuit\$1} or {voltage adj resulator\$1}	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S7	380444	{radio adj frequenc\$3}	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S8	533645	"RF"	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 18:12
S9	711533	S7 or S8	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 18:12
S10	394269	signal with path\$1	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S11	880017	{substrate or wafer} with semiconductor\$1	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12

S12	1343743	module\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S13	10	S2 and S5 and S6 and S9 and S10 and S11 and S12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S14	27	S2 and S5 and S6 and S9 and S10 and S12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S15	78177	first adj ((integrat\$3 adj circuit\$1) or die\$1 or chip \$1 or semiconductor\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S16	72527	second adj ((integrat\$3 adj circuit\$1) or die\$1 or chip \$1 or semiconductor\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S17	57673	S15 and S16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S18	880017	(substrate or wafer) with semiconductor\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S19	1461082	(signal with condition\$3) or (amplif\$4 adj circuit\$1) or amplifier\$1 or (power adj amplifier\$1) or (low adj noise adj amplifier\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S20	1111154	"PA" or "LNA"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 18:12

S21	2465807	S19 or S20	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S22	380444	(radio adj frequenc\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S23	533645	"RF"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 18:12
S24	711533	S22 or S23	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S25	2691	S17 and S21 and S24	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S26	3420	(ancillary with circuit\$1) or (voltage adj regulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S27	7	S25 and S26	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S28	0	("2007/0259639").URPN.	USPAT	OR	OFF	2010/04/08 18:12
S29	1	2005-322513.NRAN.	DERWENT	OR	OFF	2010/04/08 18:12
S30	4	("6806771").URPN.	USPAT	OR	OFF	2010/04/08 18:12

S31	34	{"3530395" "4250556" "4855685" "5068823" "5107146" "5196740" "5285169" "5311142" "5363055" "5416484" "5463349" "5510738" "5528179" "5554957" "5557234" "5574678" "5631606" "5680070" "5821776" "5877536" "5877612" "5912567" "5912583" "5952867" "5959871" "5966047" "5994926" "6014097" "6043709" "6111437" "6118266" "6140867" "6201429").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2010/04/08 18:12
S32	71527	ancillary or (voltage adj regulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S33	26	S25 and S32	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S34	68018	(ancillary with circuit\$1) or (voltage adj regulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S35	105	S25 and S34	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S36	14757	"BICMOS"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 18:12
S37	15	S35 and S36	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S38	4	"7302247"	USPAT	OR	OFF	2010/04/08 18:12

S39	3	"7185244"	USPAT	OR	OFF	2010/04/08 18:12
S40	58172	{ancillary with circuit\$1} or {(voltage adj regulator\$1) and circuit\$1}	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S41	9945	{ancillary with circuit\$1} or {(voltage adj regulator\$1) adj circuit\$1}	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S42	78177	first adj {(integrat\$3 adj circuit\$1) or die\$1 or chip \$1 or semiconductor\$1}	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S43	72527	second adj {(integrat\$3 adj circuit\$1) or die\$1 or chip \$1 or semiconductor\$1}	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S44	57673	S42 and S43	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S45	136	S41 and S44	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S46	14757	"BICMOS"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 18:12
S47	4	S45 and S46	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12

S48	90	("3713148" "3714540" "3859624" "4523128" "4536715" "4650981" "4675579" "4763075" "4785345" "4868647" "4924210" "4931867" "4937468" "4959631" "5095357" "5128729" "5168863" "5369666" "5384808" "5404545" "5442303" "5444740" "5448469" "5469098" "5555421" "5572179" "5591996" "5615091" "5615229" "5625265" "5650357" "5654984" "5663672" "5716323" "5774791" "5781071" "5781077" "5786763" "5789960" "5801602" "5812597" "5812598" "5845190" "5850436" "5864607" "5900683" "5907481" "5913817" "5926358" "5945728" "5952849" "5969590" "6049258" "6061009" "6069802" "6087882" "6114937" "6124756" "6137372" "6222922" "6232902" "6249171" "6262600" "6291907" "6307497" "6384763" "6389063" "6452519" "6525566" "6603807" "6611051" "6670861" "6728320" "6747522" "6833800" "6873065" "6903578" "6914547" "6922080" "6927662" "6940445" "6956727" "6967513" "6977522" "7016490" "7023372" "7053807" "7057491" "7075329" "7102388"). PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2010/04/08 18:12
S49	44	battery near2 (substitute or (back\$1up or backup)) and (regulator near4 voltage) same (second or additional or two) and ((regulator near4 voltage) same (second or additional or two) same chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S50	10	("5844853").URPN.	USPAT	OR	OFF	2010/04/08 18:12

S51	3	((("5144585") or ("4994688") or ("5297097")).PN.	USPAT	OR	OFF	2010/04/08 18:12
S52	1	("5485509").PN.	USPAT	OR	OFF	2010/04/08 18:12
S53	3	((("5144585") or ("4994688") or ("5297097")).PN.	USPAT	OR	OFF	2010/04/08 18:12
S54	1	("5485509").PN.	USPAT	OR	OFF	2010/04/08 18:12
S55	0	mask\$1slice adj technique	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S56	15	master\$1slice adj technique	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S57	753	"256" adj Mbit adj "DRAM"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S58	1249684	chip\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S59	397	S57 and S58	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S60	8	("3.3V" adj DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S61	3	("2.5V" adj DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12

S62	8	S60 and S60	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S63	0	S59 and S62	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S64	0	S60 and S61	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S65	158	("3.3V" with DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S66	195	("2.5V" with DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S67	50	S65 and S66	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S68	0	S67 and S69	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S69	0	S60 and S67	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S70	11	"3.3V" adj ("DRAM" or (dynamic adj random adj access adj memory))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12

S71	9	"3.3V" adj ("DRAM" or (dynamic adj random adj access adj memory))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S72	2	((("5258325") or ("5731945")).PN.	USPAT	OR	OFF	2010/04/08 18:12
S73	4247015	(integrat\$3 adj circuit\$1) or die\$1 or chip\$1 or semiconductor\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S74	1111154	"PA" or "LNA"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 18:12
S75	3420	(ancillary with circuit\$1) or (voltage adj resulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S76	380444	(radio adj frequenc\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S77	533645	"RF"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 18:12
S78	533645	"RF"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 18:12
S79	3420	(ancillary with circuit\$1) or (voltage adj resulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S80	0	("2007/0259639").URPN.	USPAT	OR	OFF	2010/04/08 18:12
S81	1	2005-322513.NRAN.	DERWENT	OR	OFF	2010/04/08 18:12

S82	4	("6806771").URPN.	USPAT	OR	OFF	2010/04/08 18:12
S83	34	("3530395" "4250556" "4855685" "5068823" "5107146" "5196740" "5285169" "5311142" "5363055" "5416484" "5463349" "5510738" "5528179" "5554957" "5557234" "5574678" "5631606" "5680070" "5821776" "5877536" "5877612" "5912567" "5912583" "5952867" "5959871" "5966047" "5994926" "6014097" "6043709" "6111437" "6118266" "6140867" "6201429").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2010/04/08 18:12
S84	71527	ancillary or (voltage adj resulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S85	14757	"BICMOS"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 18:12
S86	4	"7302247"	USPAT	OR	OFF	2010/04/08 18:12
S87	3	"7185244"	USPAT	OR	OFF	2010/04/08 18:12
S88	9945	(ancillary with circuit\$1) or ((voltage adj regulator\$1) adj circuit\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S89	14757	"BICMOS"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/04/08 18:12

S90	90	("3713148" "3714540" "3859624" "4523128" "4536715" "4650981" "4675579" "4763075" "4785345" "4868647" "4924210" "4931867" "4937468" "4959631" "5095357" "5128729" "5168863" "5369666" "5384808" "5404545" "5442303" "5444740" "5448469" "5469098" "5555421" "5572179" "5591996" "5615091" "5615229" "5625265" "5650357" "5654984" "5663672" "5716323" "5774791" "5781071" "5781077" "5786763" "5789960" "5801602" "5812597" "5812598" "5845190" "5850436" "5864607" "5900683" "5907481" "5913817" "5926358" "5945728" "5952849" "5969590" "6049258" "6061009" "6069802" "6087882" "6114937" "6124756" "6137372" "6222922" "6232902" "6249171" "6262600" "6291907" "6307497" "6384763" "6389063" "6452519" "6525566" "6603807" "6611051" "6670861" "6728320" "6747522" "6833800" "6873065" "6903578" "6914547" "6922080" "6927662" "6940445" "6956727" "6967513" "6977522" "7016490" "7023372" "7053807" "7057491" "7075329" "7102388"). PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2010/04/08 18:12
S91	44	battery near2 (substitute or (back\$1up or backup)) and (regulator near4 voltage) same (second or additional or two) and ((regulator near4 voltage) same (second or additional or two) same chip)	US-PGPUB; USPAT; USOCR; FFRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S92	10	("5844853").URPN.	USPAT	OR	OFF	2010/04/08 18:12

S93	3	((("5144585") or ("4994688") or ("5297097")).PN.	USPAT	OR	OFF	2010/04/08 18:12
S94	1	("5485509").PN.	USPAT	OR	OFF	2010/04/08 18:12
S95	3	((("5144585") or ("4994688") or ("5297097")).PN.	USPAT	OR	OFF	2010/04/08 18:12
S96	1	("5485509").PN.	USPAT	OR	OFF	2010/04/08 18:12
S97	0	mask\$1slice adj technique	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S98	15	master\$1slice adj technique	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S99	753	"256" adj Mbit adj "DRAM"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S100	8	("3.3V" adj DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S101	3	("2.5V" adj DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S102	8	S100 and S100	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S103	0	S100 and S101	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12

S104	158	("3.3V" with DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S105	195	("2.5V" with DRAM)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S106	50	S104 and S105	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S107	0	S100 and S99	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S108	11	"3.3V" adj ("DRAM" or (dynamic adj random adj access adj memory))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S109	9	"3.3V" adj ("DRAM" or (dynamic adj random adj access adj memory))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S110	9	(first adj ancillary adj circuit) same (second ancillary adj circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S111	1	("6734553").PN.	USPAT	OR	OFF	2010/04/08 18:12
S112	0	(first adj ancillary adj circuit) same (first adj chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12

S113	1	(first adj ancillary adj circuit) same (first adj ic)	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S114	0	(first adj ancillary adj circuit) same chip	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S115	0	(second adj ancillary adj circuit) same chip	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S116	64	(ancillary adj circuit) same chip	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S117	9	(ancillary adj circuit) with integrat\$3 with chip	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S118	15	(ancillary adj circuit) same integrat\$3 with chip	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S119	6	S118 not S117	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S120	16	(ancillary adj circuit) with integrat\$3 with (chip or die)	US-PGRUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S121	1	2008-A73628.NPAN.	DERWENT	OR	OFF	2010/04/08 18:12
S122	1	2008-A73628.NPAN.	DERWENT	OR	OFF	2010/04/08 18:12

S123	16	(ancillary adj circuit) with integrat\$3 same (chip or die)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12
S124	11	("20010011926" "20030165052" "5258325" "5731945" "5844853" "5983089" "6054900" "6075995" "6734553" "7164905" "7245884").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2010/04/08 18:12
S125	0	("7409200").URPN.	USPAT	OR	OFF	2010/04/08 18:12
S126	6	(ancillary adj circuit\$1) same (die\$1 or chip\$1) same (signal adj condition \$3 adj circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/04/08 18:12

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